

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>Applicant:</b>	Hiroyuki Nakajima	<b>Examiner:</b>	Brian P. Johnson
<b>Serial No.:</b>	10/785,671	<b>Art Unit:</b>	2183
<b>Filed:</b>	February 24, 2004	<b>Docket:</b>	17472
<b>For:</b>	PROCESSOR, SYSTEM LSI CIRCUIT, METHOD OF DESIGNING SAME, AND RECORDING MEDIUM HAVING SAID METHOD RECORDED THEREON	<b>Dated:</b>	December 3, 2008

**Confirmation No.:** 2687

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 23313-1450

**AMENDMENT AND RESPONSE UNDER 37 C.F.R. §1.111**

Sir:

This paper is responsive to the Office Action mailed September 12, 2008 in the above-captioned application. Favorable reconsideration and withdrawal of all rejections is kindly requested in light of the following amendments and accompanying remarks. Please amend the Application as follows.

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**CERTIFICATE OF ELECTRONIC FILING**

I hereby certify that this correspondence is being deposited with the United States Patent & Trademark Office via Electronic Filing through the United States Patent and Trademark Office e-business website, on the date below.

Dated: December 3, 2008

  
Katherine R. Vieyra